

## Lecture

Module designation	CE631 Embedded System Design		
Semester(s) in which the module is taught	5		
Person responsible for the module	Dareen Kusuma Halim		
Language	English & Indonesian		
Relation to curriculum	Compulsory		
Teaching methods	Lecture		
Workload (incl. contact hours, self-study hours)	Total workload: 90.72 hours - 23.34 hours of synchronous lecture & demonstration. - 56.04 hours of self-study and assignments in the form of mini projects. - 11.34 hours related to exam and self study		
Credit points	2 SKS (3.36 ECTS)		
Required and recommended prerequisites for joining the module	Required: - CE432 Microprocessor System - CE439 Interfacing & Instrumentation - EEM411 Electronic Fundamentals		
Module objectives/intended learning outcomes	<b>Course Learning outcome</b>	<b>Related ELOs</b>	
		<b>ELO</b>	<b>Performance Indicator</b>
	Students are able to apply the proper embedded system design steps according to the given problems	G	Understand the concept of electronics, analog systems, and digital systems in designing embedded systems.
	Students are able to analyze system requirements in relation to system design.	G	Understand the concept of electronics, analog systems, and digital systems in designing embedded systems.
	Students are able to optimize an embedded system by its hardware and software.	I	Understand the concept of software and hardware integration, distributed system, and computer communication protocols.
Content	This course covers the principles of embedded system design and modeling, emphasizing on efficient integration of hardware and software, as well as system robustness.		

	<p>Specifically, this course contain these topics:</p> <ol style="list-style-type: none"> <li>1. Introduction to embedded systems</li> <li>2. Embedded systems architecture</li> <li>3. Hardware and software design of embedded systems</li> <li>4. Testing methods for embedded systems</li> <li>5. Hardware drivers</li> <li>6. System flow management</li> <li>7. Inter-devices communications</li> <li>8. Firmware update</li> <li>9. Architecture breakdown of a specific processor</li> <li>10. Embedded software optimization</li> <li>11. Managing power consumption</li> </ol>
Examination forms	Written test
Study and examination requirements	<p>Total score <math>\geq 55</math> must be satisfied.</p> <p>The total score is the weighted average of the assignments (30%), the midterm exam (30%), and the final exam (40%).</p>
Reading list	<ol style="list-style-type: none"> <li>1. White, E. 2011. Making Embedded Systems: Design patterns for Great Software. O'Reilly Media, Inc.</li> <li>2. A Wicaksana, DK Halim, D Hartono, F Lokananta, SW Lee, MS Ng and CM Tang. 2018. Case Study: First-Time Success ASIC Design Methodology Applied to a Multi-Processor System-on-Chip. DOI: 10.5772/intechopen.79855 (InTech)</li> <li>3. D.K. Halim, C.M. Tang, M.S. Ng., and D. Hartono, "Software-based turbo decoder implementation on low power multi-processor system-on-chip for Internet of Things," 2017 4th International Conference on New Media Studies (CONMEDIA), Yogyakarta, 2017, pp. 137-141, doi: 10.1109/CONMEDIA.2017.8266045.</li> </ol>

## Lab

Module designation	CE631L Embedded System Design Lab
Semester(s) in which the module is taught	5
Person responsible for the module	Dareen Kusuma Halim
Language	English & Indonesian
Relation to curriculum	Compulsory
Teaching methods	Demonstration, Cooperative Learning
Workload (incl. contact hours, self-study hours)	<p>Total workload: 45.36 hours</p> <ul style="list-style-type: none"> <li>- 23.35 hours of group project (in-class assistance)</li> <li>- 16.34 hours of self-conducted group project and assignments related to the group project</li> </ul>

	- 5.67 hours related to exam and self study		
Credit points	1 SKS (1.68 ECTS)		
Required and recommended prerequisites for joining the module	Required: - CE432 Microprocessor System - CE439 Interfacing & Instrumentation - EEM411 Electronic Fundamentals		
Module objectives/intended learning outcomes	<b>Course Learning outcome</b>	<b>Related ELOs</b>	
		<b>ELO</b>	<b>Performance Indicator</b>
	Students are able to design an embedded system as a solution for particular problems, with the proper methodology	G I	Ability to develop embedded systems by means of appropriately combining software and hardware as a solution for user problems.  Ability to develop and integrate software and hardware as scalable distributed systems that incorporate various device types for the purpose of solving engineering problems.
	Students are able to plan and execute embedded system tasks in a group	D	Ability to operate and coordinate (if necessary) on technical working tasks in a team.
Content	This course covers the basics of developing a functional embedded system with focus on modularity, usability, and maintenance.  Specifically, this course contain these topics: 1. Embedded system as a solution 2. Embedded system constraints and requirements 3. Modularity (hardware) 4. Modularity (software) 5. Modularity (testing) 6. System communication flow 7. Power circuitry 8. Embedded system usability		

	9. Embedded system documentation
Examination forms	Project, Observation
Study and examination requirements	Total score $\geq 55$ must be satisfied. The total score is the weighted average of the assignments (30%), the midterm exam (30%), and the final exam (40%).
Reading list	1. White, E. 2011. Making Embedded Systems: Design patterns for Great Software. O'Reilly Media, Inc.